

# QA-XS&QB-XLS Series

#### 0.25W, FIXED INPUT, ISOLATED&UNREGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER



## **FEATURES**

- ♦Small Footprint
- ♦1KVDC Isolation
- ♦SIP Package
- Internal SMD Construction
- ◆Temperature Range: -40°Cto +85°C
- ♦No Heat sink Required
- ◆No External Component Required
- ◆Industry Standard Pin out

# $\frac{\text{MODEL SELECTION}}{\text{QB}^{\circ} 05^{\circ} 05^{\circ} \underline{X}^{\circ} \underline{\text{LS}}^{\circ}}$

Product Series
 Output Voltage
 SIP-7 Package

②Input Voltage④Fixed Input

### **APPLICATIONS**

The QA-XS&QB-XLS Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- 1) Where the voltage of the input power supply is fixed (voltage variation  $\pm 10\%$ );
- 2) Where isolation is necessary between input and output (isolation voltage ±1000VDC);
- 3) Where the regulation of the output voltage
- and the output ripple noise are not demanding. Such as: purely digital circuits,ordinary low

frequency analog circuits, and IGBT power device driving circuits.



PRODUCT	PROGR	AM				
	Input					
Part Number	Voltage (VDC)		Voltage	Current (mA)	Efficiency (%, Typ)	
Number	Nominal	Nominal	(VDC)	Max	(,0, 1)p)	
QB0303XLS	3.3	3.0-3.6	3.3	75.8	62	
QB0305XLS	5.5	5.0-5.0	5	50	65	
QA0505XS			±5	±25	62	
QA0509XS			±9	±13.8	64	
QA0512XS		4.5-5.5	±12	±10.4	66	
QA0515XS	5		±15	±8.3	65	
QB0505XLS			5	50	64	
QB0509XLS	_		9	27.8	65	
QB0512XLS			12	20.8	67	
QB0515XLS			15	16.7	65	
QA1205XS		10.8-13.2	±5	±25	62	
QA1209XS			±9	±13.8	63	
QA1212XS			±12	±10.4	64	
QA1215XS			±15	±8.3	65	
QB1203XLS	12		3.3	75.8	62	
QB1205XLS			5	50	65	
QB1209XLS			9	27.8	66	
QB1212XLS			12	20.8	67	
QB1215XLS			15	16.7	66	
QA2405XS			±5	±25	63	
QA2409XS			±9	±13.8	64	
QA2412XS			±12	±10.4	65	
QA2415XS			±15	±8.3	65	
QB2405XLS	24	21.6-26.4	5	50	63	
QB2409XLS			9	27.8	63	
QB2412XLS			12	20.8	65	
QB2415XLS			15	16.7	65	
QB2424XLS			24	10.4	64	

COMMON SP	ECIFICATIONS				
Item	Test conditions	Min	Тур	Max	Units
Storage humidity				95	%
Operating Temperature		-40		85	
Storage Temperature		-55		125	°C
Temp. rise at full load			15	25	
Lead temperature	1.5mm from case for 10 seconds			300	
Short circuit protection*				1	s
Cooling		Free air convection			
Case material		Plastic (UL94-V0)			
MTBF		3500			K hours
Weight			2.1		g

\*Supply voltage must be discontinued at the end of short circuit duration.

# MICROPC Industry Power Family

# QA-XS&QB-XLS Series

ISOLATION SPECIFICATIONS						
Item	Test conditions	Min	Тур	Max	Units	
Isolation voltage	Tested for 1 minute and 1 mA max	1000			VDC	
Isolation resistance	Test at 500VDC	1000			MΩ	

Item	Test co	nditions	Min	Тур	Max	Units
Output power					0.25	w
Line regulation	For Vin change	(3.3V input)			±1.5	
Line regulation	of $\pm 1\%$	(Others input)			±1.2	
Load regulation	10% to 100% load	(3.3V output)		12	20	%
		(5V output)		10.5	15	
		(9V output)		8.3	15	
		(12V output)		6.8	15	
		(15V output)		6.3	15	
Output voltage accuracy		S	ee tolerance e	nvelope grapl	h	
Temperature drift	100% full load	100% full load			0.03	%/℃
Ripple & Noise*	20MHz Bandwidt	n		50	75	mVp-p
Switching frequency	Full load, nominal	l input		100		KHz

## APPLICATION NOTE

#### Requirement on output load

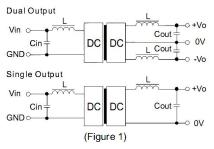
To ensure this module can operate efficiently and reliably, During operation, the minimum output load is not less than 10% of the full load, and that this product should never be operated under no load! If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

#### **Overload Protection**

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

#### Recommended circuit

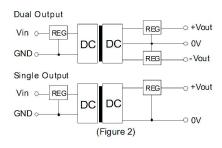
If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).



it should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. It's not recommended to connect any external capacitor in the application field.

#### Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).



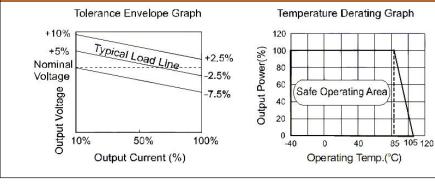
No parallel connection or plug and play.

Note:

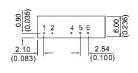
1. All specifications measured at T A =25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.

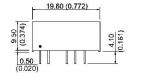
See below recommended circuits for more details.
 Dual output models unbalanced load: ±5%.

## TYPICAL CHARACTERISTICS



#### **OUTLINE DIMENSIONS & PIN CONNECTIONS**





Note: Unit:mm(inch)

Pin section:0.50\*0.30mm(0.020\*0.012inch) Pin section tolerances:±0.10mm(±0.004inch) General tolerances:±0.25mm(±0.010inch) First Angle Projection ← ⊕ RECOMMENDED FOOTPRINT Top view, grid: 2.54\*2.54mm(0.1\*0.1inch), diameter: 1.00mm(0.039inch)

-	<u>t</u> -	F+	-+-	1-		-
i i	1	2	4	5	6	1
	-	+ -		-		_1
		-			10 1	
ini	gle	ι Οι	tpu	t		

t	1	2	4	6	1
F-	-	╞┥		- + -	-1

# Pin Single Dual 1 Vin Vin 2 GND GND 4 0V -Vo

2	GND	GND
4	0V	-Vo
5	No Pin	0V
6	+Vo	+Vo