

2F-XSR Series

FIXED INPUT,ISOLATED®ULATED Single Output DC/DC Converter



FEATURES

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◆ ROHS	compliant

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◆Wide temperature performance at full 2 Watt load,-40°C to 85°C

♦UL 94V-0 package material

◆No heatsink required

◆Low ripple and good EMC Features

◆Industry standard pinout

◆Power sharing on output

◆3KVDC isolation

◆Continuous Short Circuit Protection

◆Internal SMD construction

◆No external components required

◆Good dynamic feature

MODEL SELECTION 2F⁰05⁰05⁰X⁰ S⁰R⁰

①Product Series ③Output Voltage ⑤SIP Package ②Input Voltage ④Fixed Input

®Regulated output

DESCRIPTION

The 2F-XSR series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- 1) where the voltage of the input power supply is fixed (voltage variation ≤±5%);
- 2) where isolation is necessary between input and output (isolation voltage ≤3000VDC);
- 3) where the regulation of the output voltage and the output ripple noise are demanded.





SELECTION GUIDE								
	Input Output		Efficiency	Switching				
Order code	Voltag	ge(VDC)	Voltage	Currer	nt(MA)	,	Frequency	
	Nominal	Range	(VDC)	Max	Min	(%,Typ)	(KHz,Typ)	
2F0505SR	5	4.75-5.25	5	400	40	70	333	
2F1205SR	12	11.4-12.6	5	400	40	70	58	
2F2405SR	24	22.8-25.2	5	400	40	71	66	

NOTICE:add Suffix "P" for Continuous Short Circuit Protection, e.g. IF0505XSP

ISOLATION SPECIFICATIONS						
Parameter	Test conditions	Min.	Тур.	Max.	Units	
Isolation test voltage	Flash tested for 1 minute and 1mA max	3000			VDC	
Isolation resistance	Test at Viso=500VDC	1000			ΜΩ	
Isolation capacitance			60		PF	

OUTPUT SPECIFICATIONS					
Parameter	Test conditions	Min	Тур.	Max.	Units
Output power		0.2		2	w
Line regulation	For Vin change of ±5%			±0.5	%
Load regulation	10% to 100% full load			±1.5	%
Output voltage accuracy	100% full load			±3	%
Temperature drift	100% full load			0.03	%/°C
Output Ripple*	20MHz Bandwidth		20	30	MV p-p
Output Noise*	20MHz Bandwidth		50	150	MV p-p
Switching frequency	Full load,nominal input		100		Khz

^{*} Test ripple and noise by "parallel cable" method.

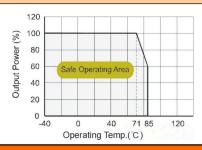
See detailed operation instructions at Testing of Power Converter section, application notes.



TEMPERATURE CHARACTERISTICS Тур. Units Conditions Min. Parameter Max Storage humidity range 95 % NO-load power consumption 10 °C Operating temperature -40 85 °C Storage temperature -55 125 °C Lead temperature 1.5mm from case for 10 seconds 300 Temp.rise at full load 58 °C 40 Cooling Free air convection Plastic(UL94-V0) Case material IF-XSP Continuous Short circuit protection MTBF 3500 K hours Weight 2.8 g

TYPICAL CHARACTERISTICS

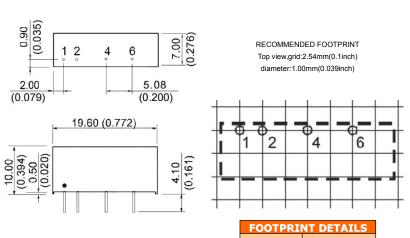
Temperature Derating Graph



OUTLINE DIMENSIONS & PIN CONNECTIONS

IF-XS SIP7

SIZE Graph

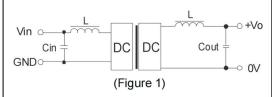


FOOTPRINT DETAILS			
Pin	Function		
1	Vin		
2	GND		
4	0V		
6	+V0		

2F-XSR Series

Recommended circuit

If you want to further decrease the input/output ripple,an "LC"filtering network may be connected to the input and output ends of the DC/DC converter. see (Figure 1).



It should also be noted that the inductance and the frequency of the "LC"filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1).

EXTERNAL CAPACITOR TABLE (TABLE 1)

Vin	Cin	Vout	Cout
(VDC)	(µ F)	(VDC)	(μ F)
5	4.7	5	4.7
12	2.2	-	-
24	0.47	-	-

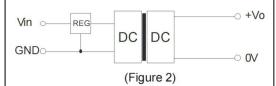
It's not recommend to connect any external capacitor in the application field with less than 0.5 watt output.

Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against over-current and short-circuits. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

Input Over-voltage Protection Circuit

The simplest device for input over-voltage protection is a linear voltage regulator with overheat protection that is connected to the input end in series(Figure 2).



When the environment temperature is higher than 71℃, the product output power should be less then 60% of the rated power.

No parallel connection or plug and play.

Use dual output simultaneously, forbid opening output pin (0V) to use as single output.



Pin section:0.50*0.3mm(0.020*0.012inch) Pin section tolerances: ±0.10mm(±0.004inch) General tolerances: ±0.25mm(±0.010inch)

Note

Microdc Professional Power Module, Inc. Tel:0086-20-86000646 E-mail:tech@microdc.cn Website:http://www.microdc.cn



RoHS COMPLIANT INFORMATION

This series is compatible with RoHS soldering systems with a peak wave solder temperature of 300° C for 10 seconds.

The pin termination finish on the SIP package type is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The DIP types are Matte Tin over Nickel Preplate. Both types in this series are backward compatible with Sn/Pb soldering systems.



REACH COMPLIANT INFORMATION

This series has proven that this product does not contain harmful chemicals, it also has harmful chemical substances through the registration, inspection and approval.

^{*}Supply voltage must be discontinued at the end of short circuit duration.